



## CALL FOR PAPERS AND EXHIBITION

upcoming : [www.erts2020.org](http://www.erts2020.org)**ORGANISERS**

- **3AF**, the Society for Aeronautics and Space
- **SEE**, the Society for Electricity, Electronics, and Information and Communication Technologies

**PROGRAMME COMMITTEE CHAIR**

- **Jean Arlat**, LAAS-CNRS, seconded from CNRS, as Scientific Advisor to the French Embassy in the UK

**GENERAL CO-CHAIRS**

- **Charles Champion**, chairman of the Toulouse-Blagnac Airport supervisory board
- **Alexandre Corjon**, Global Vice President, Electrics, Electronics and System, Alliance Renault-Nissan

Created by the late Jean-Claude Laprie in 2002, the ERTS congress is a unique biennial European cross-sector event on Embedded Real Time Systems, a forum for top-level scientists with representatives from universities, research centres, agencies and industries. The previous edition gathered more than 100 talks, 400 participants and 50 exhibitors.

ERTS is:

- a congress with high level scientific and technical presentations,
- an exhibition forum covering a wide range of innovative products and services, for improving direct relationship between providers and users,
- a unique community spirit: knowledge sharing, unforgettable moments, and networking opportunities.

The congress targets all the domains where embedded systems are crucial, such as, but not limited to, transportation (aeronautics, automotive, railway, subway, marine, unmanned vehicle, space exploration and satellite applications), e-healthcare, smart agriculture, industry, defence, energy, and telecom domains.

The congress welcomes submissions of research contribution, research - industrial transfer, industrial feedbacks on new technologies, and position papers on major trends. This 10<sup>th</sup> edition encourages in particular contributions bringing innovative cross-fertilising ideas for safe and secure intelligent systems within all critical applications featuring advanced autonomy.

**IMPORTANT DATES****CALL FOR PAPERS**

- Regular abstract and short paper submission (4 pages) **June 9<sup>th</sup>, 2019** - extended to **June 25<sup>th</sup>**
- Acceptance notification **Sept. 18<sup>th</sup>, 2019**
- Regular paper for review (10 pages) **Oct. 15<sup>th</sup>, 2019**
- Final paper (Short and Regular) **Nov. 10<sup>th</sup>, 2019**
- Congress **Jan. 29<sup>th</sup> - 31<sup>st</sup>, 2020**

**EXHIBITION SCHEDULE**

- Information and registration **March 19<sup>th</sup>, 2019 to Jan. 10<sup>th</sup>, 2020**
- Select your contact for meetings **Jan. 10<sup>th</sup> - 22<sup>th</sup>, 2020**
- Exhibition and B2B meeting **Jan. 29<sup>th</sup> - 30<sup>th</sup>, 2020**

Join ERTS for high quality papers and for high quality audience!

## CALL FOR PAPERS

Original and unpublished submissions are solicited, which may be under two forms: Regular and Short papers.

- **Regular papers** presenting achieved results. The selection will be based firstly on the review of extended abstract (4 pages) followed by the review of the full paper (10 pages) for final acceptance. Accepted regular papers will be published in the proceedings and presented in an oral session at the congress (registration and presentation by one author are mandatory).
- **Short papers** reporting on work in progress. The selection will be based on the review of the full short paper (4 pages). Each accepted short paper will be published in the proceedings and presented both as a poster and in a five-minute oral presentation at the congress (registration and presentation by one author are mandatory).

Submission are expected in all aspects of critical embedded real-time systems, and notably on the following topics:

- **Embedded computing platforms and networked systems:** Distributed architectures, Multi-core/multicore platforms, FPGA, Embedded networks, Middleware, Virtualisation, Service-oriented platforms, Energy-aware management
- **Processes, methods and tools:** Agile techniques, AI based development methods, Model-based system engineering, Virtual engineering and simulation, Formal methods, Requirement engineering, Product line engineering, New programming, and verification languages
- **Resilience:** Dependability, Safety, Cyber security, Quality of service, Fault tolerance, Maintainability, Diagnosis generation, Health monitoring, Certification
- **Human system interactions:** User interfaces for command and control systems, Human error evaluation, User-centred design, Automation design, and function allocation
- **Autonomous and connected systems:** Adaptive systems and artificial intelligence, Robotics, Drones, Internet of Things (IoT), Intelligent systems, Ethical and legal issues

Submission guidelines on the upcoming web site <http://www.erts2020.org>.

## EXHIBITION - JANUARY 29<sup>th</sup> - 30<sup>th</sup>

The exhibition is the opportunity for companies to present their latest products; it will be combined with B2B meetings for improved direct relationship between exhibitors and contractors represented by R&D directors, technical directors and projects managers from all the industry sectors. The exhibition and congress areas are situated under the same roof allowing attendees to visit booths during the congress.

Exhibition contact: [xsicard@advbe.com](mailto:xsicard@advbe.com)

## COMMITTEES

### Programme Committee

**Chair:** **Jean Arlat** (LAAS-CNRS), seconded from CNRS, as Scientific Advisor to the French Embassy in the UK

**Coordinators:** **Jean-Paul Blanquart** (Airbus Defence and Space), **Philippe Cuenot** (IRT Saint Exupéry seconded from Continental Automotive), **Kevin Delmas** (Onera), **Hervé Delseny** (Airbus), **Adrien Gauffriau** (Airbus), **Christophe Grand** (Onera), **Mohamed Kaâniche** (LAAS-CNRS), **Claire Pagetti** (Onera)

*Programme Committee members are industrial and academic experts from more than ten countries. The list is available on the website.*

### Organising Committee

**Chairs:** **Francis Guimera** (3AF), **Dominique Mary** (SEE)

*The list of the Organising Committee members will be available on the website*

### Exhibition

**Chair:** **Xavier Sicard** (ABE)

